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SPECIFICATION

RADIO COMMUNICATIONS APPARATUS

5 TECHNICAL FIELD

[0001]

The present invention relates to a radio communications apparatus and radio communication method, as well as a mixer for use therein, and more particularly, to improvements in a radio communications system which
10 simultaneously sends a local oscillation signal and a radio frequency band signal.

BACKGROUND ART

[0002]

The following Document 1, for example, discloses one communications
15 scheme which simultaneously sends a radio frequency band (RF) signal generated by up-converting an intermediate frequency band (IF) signal, and a local oscillation (LO) signal used for the up-conversion. The reception side of this communications scheme performs demodulation using the received LO signal and RF signal. Accordingly, a receiver can eliminate an
20 expensive local oscillator mounted therein, thus providing a low-cost communications system.

[0003]

Document 1: JP-A-2001-53640

The following Document 2 describes that in such a communications
25 scheme, the RF signal must be equal in power to the LO signal under the conditions of constant transmission power, in order to optimize the carrier to

noise ratio (C/N) index on the receiver side. For this reason, the IF input power must be set at a point at which the RF output power matches the LO output power, so that the amount of back-off from P1dB (1dB gain compression point) in a transmitter is determined by this matching point.

5 [0004]

Document 2: Yozo Shoji et al, "60 Ghz Band 64 QAM/OFDM Terrestrial Digital Broadcasting Signal Transmission by Using Millimeter-Wave Self-Heterodyne System," IEEE TRANSACTIONS ON BROADCASTING, VOL. 47, No. 3, September 2001, pp.218-227

10 Here, the back-off amount will be described with reference to Fig. 1. Referring to Fig. 1, there is shown a relationship between the output power of the LO signal (labeled LO) and the output power of the RF signal (labeled RF) to PIF which is the IF input power, where the amount of power from P1dB, which is a 1dB compression point of the RF signal, in a decreasing
15 direction is called the back-off amount. A reduction in the back-off amount brings the RF signal into a nonlinear region, causing a problem of distortions in the output. Therefore, an optimal value exists for the back-off amount, however, as mentioned above, the IF input power must be set at point A at which the RF output power matches the LO output power, so that the back-
20 off amount from P1dB in the transmitter is determined by this matching point.

[0005]

Consider herein that a conventional image rejection mixer is used in the communications scheme described in the aforementioned Patent Document 1. Fig. 2 is a diagram illustrating the configuration of a conventional image
25 signal rejection mixer. Power splitter 101 splits an LO signal generated from an LO signal oscillator into two components which are supplied to mixer 103

and mixer 104. Power splitter 102 splits an IF signal into two components which are supplied to mixer 103 and mixer 104. Mixers 103 and 104 each generate an RF signal from the LO signal and IF signal supplied thereto, and supply the RF signal and LO signal to power combiner 105. Power
5 combiner 105 combines the LO signal and RF signal delivered from mixer 103 and the LO signal and RF signal delivered from mixer 104, and delivers the combined signal with a suppression image.

[0006]

Here, power splitter 101 splits the LO signal into two components which
10 are equal in amplitude and have phase difference α . Power splitter 102 splits the IF signal into two components which are equal in amplitude and have phase difference β . Power combiner 105 combines the powers with an equal amplitude and a phase difference γ . In this event, α , β , γ are set such that the phase relationship satisfies:

$$15 \quad \alpha + \beta + \gamma = 2n\pi \quad \dots\dots (1) \text{ and}$$

$$\alpha - \beta + \gamma = (2n + 1)\pi \quad (n \text{ is an integer}) \quad \dots\dots (2)$$

in order to maximize an image signal rejection ratio. In this event, the LO power has a value determined by:

$$\alpha + \gamma = (2n + 1/2)\pi \quad \dots\dots (3)$$

20 [0007]

Incidentally, when the quality of communication varies following variations in the communication environment, good communications cannot be made unless the modulation scheme is changed to be suitable to a particular communication environment (quality of communication). Then, it
25 is known that an optimal back-off amount exists for P1dB of the transmitter output in a modulation scheme, and for this reason, if the back-off amount is

changed in line with variations in the communication environment, a problem arises in that the modulation scheme cannot be changed such that it is suitable to the communication environment.

[0008]

5 When communications are made as described in Patent Document 1 using a conventional image rejection mixer, the LO power has a value determined by a phase:

$$\alpha + \gamma = (2n + 1/2)\pi$$

10 When communications are made to result in optimal reception C/N, the back-off amount is determined to be at IF power at which the LO output power is equal to the RF output power, resulting in a failure in communications made at an back-off amount suitable to a communication environment.

DISCLOSURE OF THE INVENTION

15 [0009]

It is an object of the present invention to provide a radio communications apparatus and a radio communication method which are capable of changing an LO output power in accordance with a modulation scheme which can be changed in accordance with changes in the quality of communication due to a varying communication environment, and to provide a mixer for use in the same.

[0010]

25 A radio communications apparatus according to the present invention is a radio communications apparatus for simultaneously transmitting a radio frequency band signal and a local oscillation signal used when an intermediate frequency band signal is converted to the radio frequency band

signal by a mixer, and is characterized by comprising control means for changing a modulation scheme in accordance with the quality of communication and controlling the output power of the local oscillation signal.
[0011]

5 A radio communication method according to the present invention is a radio communication method for simultaneously transmitting a radio frequency band signal and a local oscillation signal used when an intermediate frequency band signal is converted to the radio frequency band signal, and is characterized by comprising a modulation scheme changing
10 control step for changing a modulation scheme in accordance with the quality of communication, and by comprising a step for controlling an output power of the local oscillation signal in accordance with the quality of communication.
[0012]

 A program according to the present invention is a program for causing a
15 computer to execute a radio communication method for simultaneously transmitting a radio frequency band signal and a local oscillation signal used when an intermediate frequency band signal is converted to the radio frequency band signal, and is characterized by causing the computer to execute processing for changing a modulation scheme in accordance with
20 the quality of communication, and to execute processing for controlling output power of the local oscillation signal in accordance with the quality of communication.
[0013]

 A mixer according to the present invention is a mixer in a radio
25 communications apparatus for simultaneously transmitting a radio frequency band signal and a local oscillation signal used when an intermediate

frequency band signal is converted to the radio frequency band signal, and is characterized in that output power of the local oscillation signal can be controlled by a control signal in accordance with the quality of communication.
[0014]

5 Operations of the present invention will be described. The LO output power is controlled in the RF unit simultaneously with the control of a modulation scheme for transmission signals in accordance with the quality of communication measured on the basis of a received signal received by the antenna. By conducting such control, the back-off amount can be freely
10 changed to provide an optimal modulation method in accordance with variations in radio communication environment.

BRIEF DESCRIPTION OF THE DRAWINGS

[0015]

[Fig. 1]

15 Fig. 1 is a diagram for describing a back-off amount.

[Fig. 2]

Fig. 2 is a diagram illustrating a conventional image rejection mixer.

[Fig. 3]

Fig. 3 is a schematic configuration diagram of a radio communications
20 apparatus according to the present invention.

[Fig. 4]

Fig. 4 is a schematic flow chart illustrating the operation of controller 1
in Fig. 3.

[Fig. 5]

25 Fig. 5 is a diagram illustrating the configuration of RF unit 2 in Fig. 3.

[Fig. 6]

Fig. 6 is a diagram illustrating mixer unit 12 in a first embodiment of the present invention.

[Fig. 7]

Fig. 7 is a diagram showing a change in output power with respect to a
5 change in phase, by mixer unit 12 in the first embodiment of the present invention.

[Fig. 8a]

Fig. 8a is a diagram showing the phase dependence of the input/output characteristic in mixer unit 12, when $X = (1/6)\pi$, in the first embodiment of the
10 present invention.

[Fig. 8b]

Fig. 8b is a diagram showing the phase dependence of the input/output characteristic in mixer unit 12, when $X = (1/2)\pi$, in the first embodiment of the present invention.

15 [Fig. 8c]

Fig. 8c is a diagram showing the phase dependence of the input/output characteristic in mixer unit 12, when $X = (5/6)\pi$, in the first embodiment of the present invention.

[Fig. 9]

20 Fig. 9 is a diagram illustrating an exemplary modification to a modulation scheme in controller 1 in Fig. 3.

[Fig. 10]

Fig. 10 is a diagram illustrating mixer unit 12 in a second embodiment of the present invention.

25 [Fig. 11]

Fig. 11 is a diagram illustrating the mixer unit 12 in a third embodiment

of the present invention.

Fig. 12]

Fig. 12 is a diagram illustrating mixer unit 12 in a fourth embodiment of the present invention.

5 [Fig. 13]

Fig. 13 is a circuit diagram illustrating a specific example of a mixer in Fig. 12.

[Fig. 14]

Fig. 14 is a diagram showing the bias dependence of output power in a mixer in Fig. 13.

[Fig. 15]

Fig. 15 is a diagram illustrating mixer unit 12 in a fifth embodiment of the present invention.

[Fig. 16]

15 Fig. 16 is a diagram illustrating mixer unit 12 in a sixth embodiment of the present invention.

BEST MODE FOR CARRYING OUT THE INVENTION

[0016]

20 In the following, embodiments of the present invention will be described with reference to the drawings.

(First Embodiment)

Fig. 3 illustrates the configuration of the system according to a first embodiment of the present invention. The system comprises controller 1, RF unit 2, baseband/IF unit 3, bit error rate detector 4, and antenna unit 5.

25 In the figure, bold lines represent control signal lines, while thin lines represent information signal lines (the same applies to the following). RF

unit 2 converts an RF signal received by antenna unit 5 to an IF signal. The IF signal converted by RF unit 2 is supplied to baseband/IF unit 3. This baseband/IF unit 3 demodulates data from the supplied IF signal, and supplies the data to bit error rate detector 4. Bit error rate detector 4 detects a bit error rate (BER) from the data supplied thereto, and transmits the BER, which is the result of the detection, and demodulated data to controller 1.

[0017]

Fig. 4 is a schematic flow diagram illustrating the operation of this controller 1. Controller 1 determines, based on the BER supplied thereto (step S1), an optimal modulation scheme for the BER (step S2), and determines an LO output power corresponding to the determined modulation scheme (step S3). Then, controller 1 issues a modulation scheme changing instruction to baseband/IF unit 3 (step S4), and simultaneously transmits an LO output power changing instruction to RF unit 2 (step S5).

[0018]

Baseband/IF unit 3 sets a modulation scheme for transmission data to a modulation scheme in accordance with the modulation scheme changing instruction from controller 1, modulates the transmission data in accordance with this modulation scheme, and supplies the modulated data to RF unit 2 as an IF signal. RF unit 2, in turn, changes the LO output power in accordance with the LO output power changing instruction from controller 1, and transmits an RF signal and an LO signal to antenna 5. As can be appreciated, controller 1 does not transmit the modulation scheme changing instruction to baseband/IF unit 3 or the LO output power changing instruction to RF unit 2 unless there is a change in the detected bit error rate.

[0019]

Fig. 5 is a block diagram illustrating the configuration of RF unit 2. RF unit 2 comprises LO signal oscillator 11, mixer unit 12, transmission amplifier 13, variable attenuator 14, low-noise amplifier 15, detector 16, and transmission/reception change-over switches (SW) 17, 18. LO signal oscillator 11 oscillates an LO signal for up-converting an IF signal, and supplies the LO signal to mixer unit 12. Mixer unit 12 up-converts an IF signal supplied from variable attenuator 14, using the LO signal supplied from LO signal oscillator 11, to generate an RF signal.

[0020]

Also, mixer unit 12 simultaneously supplies the generated RF signal and the LO signal used for the up-conversion to transmission amplifier 13. Mixer unit 12 also adjusts the output power of the LO signal in accordance with the LO signal output power changing instruction supplied thereto from controller 1, and supplies the adjusted LO signal to transmission amplifier 13. Transmission amplifier 13 amplifies the RF signal and LO signal supplied from mixer unit 12 to a transmission power, and supplies the amplified signals to antenna unit 5. The gain of transmission amplifier 13 is controlled in accordance with the LO signal output power changing instruction supplied from controller 1.

[0021]

Variable attenuator 14 changes the amount of attenuation provided thereby in accordance with the LO output power changing instruction supplied from controller 1, adjusts the IF power supplied from baseband/IF unit 3, and supplies the IF signal to mixer unit 12. Low-noise amplifier 15 amplifies the RF signal received by antenna unit 5, and supplies the amplified RF signal to detector 16. Detector 16 converts the RF signal supplied from

low-noise amplifier 15 to an IF signal which is supplied to baseband/IF unit 3. Transmission/reception change-over switches 17 and 18 each switch transmission/reception in accordance with a transmission/reception change-over control signal from controller 1.

5 [0022]

Fig. 6 illustrates the circuit configuration of mixer unit 12 in the first embodiment of the present invention. Mixer unit 12 comprises LO signal power splitter 201, IF signal power splitter 202, mixers 203 and 204, and RF signal power combiner 205. Active baluns, for example, are used for power
10 splitters 201 and 202 and power combiner 205. Power splitter 201 splits an LO signal into two components, which are equal in amplitude and have phase difference α , and are supplied to mixers 203 and 204.

[0023]

Power splitter 202 splits the IF signal into two components, which are
15 equal in amplitude and have phase difference β , and are supplied to mixers 203 and 204. Mixers 203 and 204 each up-convert the IF signal supplied from power splitter 202 to an RF signal frequency band with the LO signal supplied from power splitter 201 to generate an RF signal which is supplied to power combiner 205 simultaneously with the LO signal used in the up-
20 conversion. Power combiner 205 combines the RF signal and LO signal supplied from mixers 203 and 204 with phase difference γ , and delivers the combined signal. Power splitters 201 and 202, and power combiner 205 change their respective phase differences in accordance with the LO output power changing instruction supplied from controller 1 to the mixer unit. The
25 relationship among the phase differences is established to satisfy the aforementioned equation (2), i.e.,

$$\alpha - \beta + \gamma = (2n+1)\pi \quad (n \text{ is an integer})$$

in order to maximize the amount of rejection for an image signal.

[0024]

In this event, when $\alpha + \gamma = X$, $\beta = X - (2n+1)\pi$. Fig. 7 shows the result of calculating a change in LO output power and a change in RF output power with respect to phase $X (= \alpha + \gamma)$ using a high frequency circuit calculation software application. It can be understood that the LO output power and RF output power can be controlled by controlling phase X . Fig. 8a shows a change in the RF signal and LO signal delivered from mixer unit 12, with respect to the IF signal power, which is calculated using the high frequency circuit calculation software application, when $X = (1/6)\pi$. Fig. 8b shows the result of the calculation when $X = (1/2)\pi$; and Fig. 8c when $X = (5/6)\pi$, respectively.

[0025]

Assuming that mixer unit 12 output power is in a linear operation region of the transmission amplifier next to mixer unit 12, the back-off amount is determined by mixer unit 12 output. Therefore, the back-off amount, calculated for the respective phases from Figs. 8a - 8c, is 6 dB when $X = (1/6)\pi$; 11 dB when $X = (1/2)\pi$; and 17 dB when $X = (5/6)\pi$. In this way, it is understood that the back-off amount can be changed by changing phase X .

[0026]

Assume herein that in controller 1, modulation schemes as shown in Fig. 9 are employed in accordance with BER which represents the quality of communication. Specifically, assume that when the quality of communication is poor, the BPSK (Binary Phase Shift Keying) scheme, which provides the lowest information transmission speed, is employed; when the

quality of communication is moderate, the QPSK (Quadrature Phase Shift Keying) scheme, which provides a relatively high information transmission speed, is employed; and when the quality of communication is good, the 8PSK scheme, which provides a high information transmission speed, is employed. Accordingly, controller 1 determines such that $X = (1/6)\pi$ is set, as shown in Fig. 8a, for the BGPSK modulation scheme; $X = (1/2)\pi$, as shown in Fig. 8b, for the QPSK modulation scheme; and $X = (5/6)\pi$, as shown in Fig. 8c, for the 8PSK modulation scheme.

[0027]

The determination of the modulation scheme and X in this event can be readily made by previously writing an optimal modulation scheme and X value for each BER in a ROM table, and reading them in accordance with the actual BER. As can be appreciated, since the LO output power is uniquely determined when X is determined, the LO output power may be written instead of X .

[0028]

In Fig. 6, phase difference β of power splitter 202 is controlled from controller 1 because when α and γ are changed, control must be conducted to satisfy Equation (2) at all times by changing β as well in connection therewith. Also, as illustrated in Fig. 5, controller 1 controls the amount by which variable attenuator 14 attenuates an IF signal and the gain of transmission amplifier 13 for the following reason. X is controlled to variably control the back-off amount so as to change the matching point of the LO and RF output power, as shown in each of Figs. 8a - 8c, and in this event, the power of the IF signal must be changed as well, so that variable attenuator 14 is controlled for the amount of attenuation, and the gain of transmission

amplifier 13 must be controlled so that it increases in order to simultaneously compensate for the power thus attenuated.

[0029]

Assume, therefore, that after determining an optimal modulation
5 scheme, controller 1 determines X or LO output power, and simultaneously determines the amount of attenuation of variable attenuator 14, the gain of transmission amplifier 13, and phase difference β , and controller 1 control the respective components.

(Second Embodiment)

10 Fig. 10 illustrates a second embodiment of the present invention. The configuration other than mixer unit 12 is similar to the first embodiment. Mixer unit 12 in the second embodiment comprises LO signal power splitter 301, IF signal power splitter 302, LO signal phase shifter 303, IF signal phase shifter 304, RF signal phase shifter 305, RF signal power combiner 306, and
15 mixers 203 and 204.

[0030]

Power splitter 301 splits an LO signal into two components which are equal in amplitude and have phase difference α_1 ; power splitter 302 splits an IF signal into two components which are equal in amplitude and have phase
20 difference β_1 ; and power combiner 306 combines the RF signal and LO signal with phase difference γ_1 . Phase shifter 303 shifts the phase of the LO signal by δ ; phase shifter 304 shifts the phase of the IF signal by φ ; and phase shifter 305 shifts the phases of the RF and LO signals by ψ .
Wilkinson power splitters, branch line couplers, Lange couplers, and rat race
25 circuits, for example, are used for the power splitters and power combiner, and phase differences α_1 , β_1 , γ_1 may be fixed.

[0031]

On the other hand, active phase shifters, for example, are used for the phase shifters. Phase shifters 303, 304, 305 each vary the amount by which the phase is changed in accordance with an LO output power
5 changing instruction supplied from controller 1.

The relationship among the phases is established to satisfy:

$$(\alpha_1 + \delta) - (\beta_1 + \varphi) + (\gamma_1 + \psi) = (2n + 1)\pi \quad \dots (4)$$

in order to maximize the amount of rejection for an image signal. In this example,

10
$$X = (\alpha_1 + \delta) + (\gamma_1 + \psi)$$

is established, so that when α_1 and γ_1 are fixed, δ , ψ are variable. Also, like the previous embodiment, φ is a variable as well in order to satisfy Equation (4), as a matter of course, and is controlled by controller 1 together with δ , ψ .
(Third Embodiment)

15 Fig. 11 illustrates the configuration of mixer unit 12 in a third embodiment of the present invention. The configuration other than mixer unit 12 is similar to the first embodiment. Mixer unit 12 in the third embodiment comprises LO signal power splitter 301, IF signal power splitter 302, RF signal power splitter 306, LO signal phase shifter switching unit 401,
20 IF signal phase shifter switching unit 402, RF signal phase shifter switching unit 403, and mixers 203 and 204.

[0032]

Power splitters 301 and 302 and power combiner 306 are similar to those in the second embodiment. Phase shifter switching unit 401
25 comprises at least two phase shifters, and switches to any of phase shifters which provide phase changing amounts (phase shifting amounts) $\delta_1 - \delta_m$, in

accordance with an LO output power changing instruction from controller 1. Likewise, phase shifter switching unit 402 switches to $\phi_1 - \phi_m$ in accordance with the LO output power changing instruction from controller 1. Phase shifter switching units 403 switches to $\psi_1 - \psi_m$ in accordance with the LO output power changing instruction from controller 1.

[0033]

A phase shifter comprises, for example, a transmission line, an inductor, a capacitor, or a combination of them. The relationship among the phases is established to satisfy:

$$(\alpha_1 + \delta i) - (\beta_1 + \phi j) + (\gamma_1 + \psi k) = (2n + 1)\pi \quad \dots (5)$$

in order to maximize an image signal rejection amount. Each of i, j, k is 1 ~ m.

(Fourth Embodiment)

Fig. 12 illustrates the configuration of mixer unit 12 in a fourth embodiment of the present invention. The configuration other than mixer unit 12 is similar to the first embodiment. Mixer unit 12 in the fourth embodiment comprises LO signal power splitter 501, IF signal splitter 502, mixers 503 and 504, and RF signal power combiner 505.

[0034]

Power splitter 501 splits an LO signal into two components which are equal in amplitude and have phase difference α_2 . Power splitter 502 splits equally an IF signal into two components which have phase difference β_2 . Power combiner 505 combines the RF signal and LO signal with phase difference γ_2 . The phase relationship is set to satisfy:

$$\alpha_2 + \beta_2 + \gamma_2 = 2n\pi \text{ and } \alpha_2 - \beta_2 + \gamma_2 = (2n + 1)\pi$$

in order to maximize an image rejection ratio.

[0035]

An LO output power changing instruction from controller 1 is supplied to mixers 503 and 504 to change the LO power at the output of mixer unit 12.

Fig. 13 illustrates mixers 503 and 504 which are used as anti-parallel diode

5 pair mixer disclosed, for example, in Japanese Patent Application No. 2002-307136 proposed by the inventors and others of the present application.

[0036]

When this anti-parallel diode pair mixer is used, a local oscillation

frequency is one half of an LO signal frequency of a mixer output, and the

10 phase relationship between the power splitters and power combiner is set to establish:

$$2\alpha_2 + \beta_2 + \gamma_2 = 2n\pi \text{ and}$$

$$2\alpha_2 - \beta_2 + \gamma_2 = (2n + 1)\pi$$

[0037]

15 Mixers 503 and 504 each comprise anti-parallel pair diode mixer 506 which employs two diodes D1 and D2 which differ in junction area from each other (in other words, differ in impedance), and a bias unit 507 for applying a bias to this mixer. L1 represents a short stub, and L2 represents an open stub, and they are set to a length equal to the electric length of a one-half
20 wavelength of the LO signal. Anti-parallel pair diode mixer 506 is applied at the input with a signal (LO/2) at a frequency one half as high as that of the IF signal and LO signal, and delivers an RF signal and the LO signal from the output.

[0038]

25 The magnitude of a DC voltage of bias unit 507 is controlled in accordance with an LO output power changing instruction supplied from

controller 1. Fig. 14 shows changes in the LO output power and RF output power with respect to a change in DC voltage. It can be seen that as the DC voltage is increased, the LO output power increases. Accordingly, the LO output power can be controlled to be optimal for a modulation scheme by controller 1 by changing the DC voltage.

(Fifth Embodiment)

Fig. 15 illustrates the configuration of mixer unit 12 in a fifth embodiment of the present invention. The configuration other than mixer unit 12 is similar to the first embodiment. Mixer unit 12 in the fifth embodiment is similar in configuration to a mixer circuit described in Fig. 9 of Japanese Patent No. 3268574, and is configured to control the phase and amplitude, with the aid of phase shifter 608 and attenuator 609, in accordance with an LO output power changing instruction from controller 1.

[0039]

Power splitter 601 splits the LO signal into two components. One component of the LO signal split by power splitter 601 is supplied to power splitter 602, and is split with an equal amplitude and phase difference $\alpha/4$. Power splitter 603 splits the IF signal with an equal amplitude and phase difference $\beta/4$. Mixers 604 and 605 generate an RF signal from an IF signal and an RF signal split by power splitters 602 and 603, and delivers the RF signal together with the LO signal.

[0040]

The RF signal and LO signal delivered from mixers 604 and 605 are combined in power combiner 606 with phase difference $\gamma/4$. The other LO signal split by and delivered from power splitter 601 is changed in phase by phase shifter 607 in accordance with an LO output power changing

instruction from controller 1. The LO signal, which has been changed in phase, combines the RF signal and LO signal at the output of power combiner 606, with the LO signal at the output of attenuator 608.

[0041]

- 5 The phase relationship among power splitters 602 and 603 and power combiner 606 is set to satisfy:

$$\alpha_4 + \beta_4 + \gamma_4 = 2n\pi \text{ and } \alpha_4 - \beta_4 + \gamma_4 = (2n + 1)\pi$$

in order to maximize an image rejection ratio.

(Sixth Embodiment)

- 10 Fig. 16 illustrates the configuration of mixer unit 12 in a sixth embodiment of the present invention. The circuit configuration of this mixer unit 12 is similar to the first embodiment illustrated in Fig. 6, but the local oscillation signal frequency is $1/m$ (m is an integer) of an LO signal at the output of mixer unit 12. Power splitter 701 equally splits a local oscillation
15 signal LO/m , and deliver the signals with phase difference α_3 . In this event, the phase relationship is set to satisfy:

$$m \times \alpha_3 - \beta + \gamma = (2n + 1)\pi$$

- in order to maximize an image rejection ratio. Likewise, in this event, $X = m \times \alpha_3 + \gamma$ is established, and the LO output power changes by controlling X and
20 β in controller 1.

[0042]

- In the foregoing embodiments, the bit error rate (BER) for a received signal has been employed as an example of the quality of communication, but other CNR (Carrier to Noise Ratio), SNR (Signal to Noise Ratio) or the
25 like may be used as long as it is information indicative of the quality communication. Also, modulation schemes which are changed in

accordance with the quality of communication are not limited to three stages of the aforementioned BPSK, QPSK, 8PSK, but faster modulation schemes can also be added, such as 16QAM (Quadrature Amplitude Modulation) and the like.

5 [0043]

Further, instead of measuring the information of the quality of communication by bit error rate detector 4 in a receiver unit of own apparatus, as in the example illustrated in Fig. 3, signal quality information such as BER measured by a communication partner can be received, and a modulation
10 scheme and back-off amount (i.e., the LO output power) can be controlled in accordance with this. Also, the operation of controller 1, i.e., the operation flow illustrated in Fig. 4, may have been previously stored in a recording medium such as ROM as a program, which can be read and executed by a computer (CPU), as a matter of course.